Substitute for form 1449A/PTO
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(VITAS many sheets as nocessary) Complete if Known 09/239,898 **Application Number** Filing Date January 29, 1999 Seyyedy, Mirmajid First Named Inventor **Group Art Unit** 2133 Lamarre, Guy **Examiner Name** Attorney Docket No: 303.550US1 Sheet 1 of 1

OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
6		"16Mb Double Data Rate Synchronous Graphics RAM", <u>IBM Corporation</u> , IBM0616328RL6A,(December 1997),pp. 1-53	
0		"16Mbit DDR SGRAM, 128K x 32Bit x 4 Banks Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Revision 0.6", Samsung Electronics, KM432D5131,(April 1998),pp. 1-48	